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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/708,662	03/18/2004	Frank Lin	VIAP0088USA	2661
27765	7590	07/28/2005	EXAMINER	
NORTH AMERICA INTERNATIONAL PATENT OFFICE (NAIPC)				TUNG, KEE M
P.O. BOX 506				ART UNIT
MERRIFIELD, VA 22116				PAPER NUMBER
				2671

DATE MAILED: 07/28/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	<b>Application No.</b> 10/708,662	<b>Applicant(s)</b> LIN ET AL.
	<b>Examiner</b> Kee M. Tung	<b>Art Unit</b> 2671

-- *The MAILING DATE of this communication appears on the cover sheet with the correspondence address* --

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

## Status

1)  Responsive to communication(s) filed on 03 June 2005.

2a)  This action is **FINAL**.                    2b)  This action is non-final.

3)  Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

## **Disposition of Claims**

4)  Claim(s) 1-20 is/are pending in the application.  
4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.

5)  Claim(s) \_\_\_\_\_ is/are allowed.

6)  Claim(s) 1-20 is/are rejected.

7)  Claim(s) \_\_\_\_\_ is/are objected to.

8)  Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

## Application Papers

9)  The specification is objected to by the Examiner.

10)  The drawing(s) filed on \_\_\_\_\_ is/are: a)  accepted or b)  objected to by the Examiner.

Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).

Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).

11)  The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

**Priority under 35 U.S.C. § 119**

12)  Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
a)  All    b)  Some \* c)  None of:  
1.  Certified copies of the priority documents have been received.  
2.  Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
3.  Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a))

\* See the attached detailed Office action for a list of the certified copies not received

**Attachment(s)**

1)  Notice of References Cited (PTO-892)  
2)  Notice of Draftsperson's Patent Drawing Review (PTO-948)  
3)  Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  
Paper No(s)/Mail Date \_\_\_\_\_ .

4)  Interview Summary (PTO-413)  
Paper No(s)/Mail Date. \_\_\_\_\_ .

5)  Notice of Informal Patent Application (PTO-152)

6)  Other: \_\_\_\_\_ .

## **DETAILED ACTION**

The amendment filed 6/3/05 has been considered in preparing this Office action.

### ***Allowable Subject Matter***

1. Claims 1-10 are allowed.
2. The following is a statement of reasons for the indication of allowable subject matter: the prior art made of record fails to anticipate or make obvious the claimed invention. Specially, the prior art fails to teach or suggest, in combination with the remaining steps, a method for a display controller to access data stored in a memory device of a computer device comprising setting a block capacity value and wherein a total amount of data required by read requests grouped in each request group is less than the block capacity value as recited in claim 1.
3. The indicated allowability of claims 11-20 is withdrawn in view of the newly discovered reference(s) to Sadowsky et al (6,898,679). Rejections based on the newly cited reference(s) follow.

### ***Claim Rejections - 35 USC § 102***

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical

Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 11-20 are rejected under 35 U.S.C. 102(e) as being anticipated by Sadowsky et al (6,898,679 hereinafter "Sadowsky").

Sadowsky teaches a method (Figs. 2 and 3 and abstract) for accessing data , a plurality of read requests (such as, A5, A6, ..., D6, D7) uses for accessing data from a memory device (system memory, not shown, col. 2, line 32) according to a predetermined request sequence (the original sequence before reorder device 220), the method comprising reordering (reordering device 220) said read requests according to pages in said memory device accessed by said read requests in a second request sequence (reordering sequence), wherein said read requests accessed the same page of said memory device are continuously arranged (col. 2, line 56 to col. 3, line 5); and executing the read requests according to said second request sequence (col. 2, line 56 to col. 3, line 5). Therefore, at least claims 11 and 19 are anticipated by Sadowsky.

As per claim 12, Sadowsky teaches when the pages in the memory device of the read requests in the next request group include pages that are the same as the page corresponding to the final read request in the last group, executing the read requests in the next group corresponding to the data in the page and then executing the read requests in the next command block corresponding to the data of the different page (col. 2, line 56 to col. 3, line 5).

As per claim 13, Sadowsky teaches a memory controller (inherent part of the chipset or north bridge, not shown, col. 1, line 15) is used to store the plurality of read requests in a queue (FIFOs).

As per claims 14, 16 and 17, Sadowsky teaches a memory controller is installed in a north bridge circuit and the north bridge circuit is used for controlling the transmission between a display controller (part of the graphics controller or graphics card which connected to the north bridge via AGP bus) and the memory device.

As per claim 15, Sadowsky teaches data are read with a memory controller and are transmitted to a display controller (see claims 13 and 14).

As per claim 18, Sadowsky teaches the display controller is installed in a north bridge circuit in the computer device (integrated graphics chipset, col. 1, line 15).

As per claim 20, Sadowsky teaches a memory controller is used to store data in a display controller (inherent by local graphics memory or frame buffer in the graphics card or the cache 210 in Fig. 2) according to the predetermined request sequence.

#### ***Claim Rejections - 35 USC § 112***

6. Claim 20 is rejected under 35 U.S.C. 112, first paragraph, as failing to comply with the enablement requirement. The claim(s) contains subject matter which was not described in the specification in such a way as to enable one skilled in the art to which it pertains, or with which it is most nearly connected, to make and/or use the invention. The recited feature of “memory controller is used to store data in a display controller” was not described in the specification in such a way as to enable one skilled in the art to

which it pertains, or with which it is most nearly connected, to make and/or use the invention as required under 35 USC 112, 1<sup>st</sup> paragraph.

***Response to Arguments***

7. Applicant's arguments with respect to claims 1-20 have been considered but are moot in view of the new ground(s) of rejection.

***Conclusion***

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kee M. Tung whose telephone number is 571-272-7794. The examiner can normally be reached on Tuesday - Friday from 5:30 am - 4:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Ulka Chauhan can be reached on 571-272-7782. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



Kee M Tung  
Primary Examiner  
Art Unit 2671